# 2SD2240, 2SD2240A

# Silicon NPN epitaxial planar type

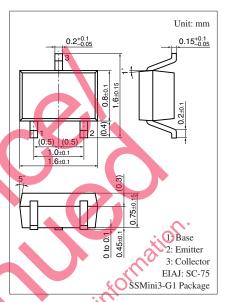
For high breakdown voltage low-frequency and low-noise amplification

#### ■ Features

- $\bullet$  High collector-emitter voltage (Base open)  $V_{CEO}$
- Low noise voltage NV
- SS-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing.

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage	2SD2240	$V_{CBO}$	150	V
(Emitter open)	2SD2240A		185	
Collector-emitter voltage	2SD2240	V <sub>CEO</sub>	150	V
(Base open)	2SD2240A		185	
Emitter-base voltage (Coll	V <sub>EBO</sub>	5	V	
Collector current		$I_{\rm C}$	50	A
Peak collector current	I <sub>CP</sub>	100	A	
Collector power dissipation	P <sub>C</sub>	125	mW	
Junction temperature	$T_{\rm j}$	125	°C	
Storage temperature	$T_{stg}$	-55 to +125	°C	



Marking Symbol:

28D2240: P 22SD2240A: L

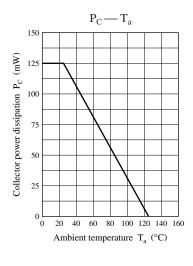
## ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

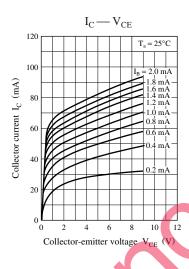
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage 2SD2240	V <sub>CEO</sub>	$I_{\rm C} = 100  \mu \text{A}  I_{\rm B} = 0$	150			V
(Base open) 2SD2240A		110, 116	185			
Emitter-base voltage (Collector open)	$V_{EBO}$	$I_E = 10 \mu\text{A}, I_C = 0$	5			V
Collector-base cutoff current (Emitter open)	I <sub>CBO</sub>	$V_{CB} = 100 \text{ V}, I_E = 0$			1	μΑ
Forward current transfer ratio *	$h_{FE}$	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	130		330	_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 30 \text{ mA}, I_B = 3 \text{ mA}$			1	V
Transition frequency	$f_T$	$V_{CB} = 10 \text{ V}, I_E = -10 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		2.3		pF
(Common base, input open circuited)						
Noise voltage	NV	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}, G_{V} = 80 \text{ dB}$		150		mV
		$R_g = 100 \text{ k}\Omega$ , Function = FLAT				

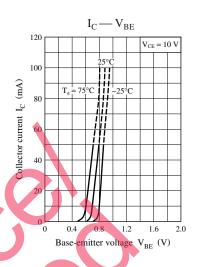
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

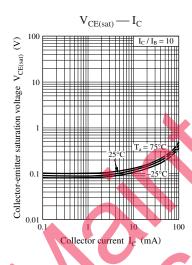
#### 2. \*: Rank classification

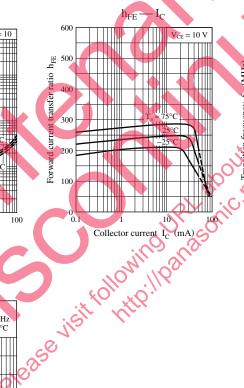
Rank	Q	R
$h_{FE}$	130 to 220	185 to 330

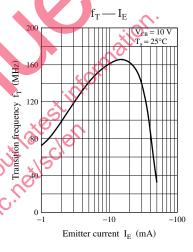


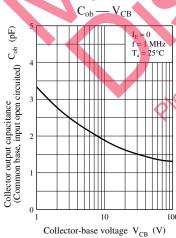












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